

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	(CMOS OR MOSFET\$2) with pinning	USPAT	OR	OFF	2005/05/31 09:37
L2	3	(CMOS OR MOSFET\$2) with pining	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/31 09:49
L3	5701	(CMOS OR MOSFET\$2) and (implant\$4 with through (gate adj electrode) with pocket\$4)	USPAT	OR	OFF	2005/05/31 09:50
L4	441	(CMOS OR MOSFET\$2) with (implant\$4 with through (gate adj electrode) with pocket\$4)	USPAT	OR	OFF	2005/05/31 09:50
L5	430	(CMOS OR MOSFET\$2) with (implant\$4 with through (gate adj electrode) with pocket\$4 with channel)	USPAT	OR	OFF	2005/05/31 09:51
L6	430	(CMOS OR MOSFET\$2) with (implant\$4 with through (gate adj electrode) with pocket\$4 with channel with (source and drain))	USPAT	OR	OFF	2005/05/31 09:51
L7	425	(CMOS OR MOSFET\$2) with (implant\$4 with through (gate adj electrode) with pocket\$4 with channel with (source and drain) with silicide)	USPAT	OR	OFF	2005/05/31 09:52
L8	425	(CMOS OR MOSFET\$2) with (implant\$4 with through (gate adj electrode) with pocket\$4 with channel with (source and drain) with (titanium adj silicide))	USPAT	OR	OFF	2005/05/31 10:49